Printec	l Page	e:- 04	Subject Code:- BCSIOT0302	
			Roll. No:	
N	[OID ₄	A INSTITUTE OF ENGINEERING	AND TECHNOLOGY, GREATER N	IOIDA
		(An Autonomous Institute A	ffiliated to AKTU, Lucknow)	
		SEM. III THEODY EVA	Tech	
		SEM. III - THEORT EAA Subject: Logic Desig	n and Microcontroller	
Time	e: 3 H	ours	Max.	Marks: 100
Genera	al Inst	ructions:		1.1.1.1.1.1.1.1.0.0
IMP: V	/erify	that you have received the question	paper with the correct course, code, b	ranch etc.
1. This	Que	stion paper comprises of three Sectio	ns -A, B, & C. It consists of Multiple (Choice
Questi	ons (l	MCQ's) & Subjective type questions.		
2. <i>Max</i>	cimun	marks for each question are indicat	ed on right -hand side of each questio	n.
3. Illus	strate	your answers with neat sketches whe	erever necessary.	
4. ASSI 5 Prot	ime si farabl	Mable data if necessary.	dor	
6 No s	crubi sheet	should be left blank. Any written mat	erial after a blank sheet will not be	
evalua	ted/cl	hecked.		
<u>SECT</u>	ION-	<u>A</u>		20
1. Atte	empt a	ıll parts:-		
1-a.	Tl	ne two's complement of 1010 is: (C	CO1,K1)	1
	(a)	101		
	(b)	1110		
	(c)	0110		
	(d)	1011	1	
1-b.	Tl in	ne prime implicant which has at least aplicant is known as (c one element that is not present in any (CO1,K1)	other 1
	(a)	Essential Prime Implicant		
	(b)	Implicant		
	(c)	Complement		
	(d)	Prime Complement		
1-c.	In pa	a 4-bit Johnson counter sequence, that terns? (CO2,K1)	here are a total of how many states or l	oit 1
	(a)	1		

- (b) 3
- (c) 4
- (d) 8

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1-d. Characteristics equation of S_R flip flop is ----- (CO2,K1)

> Qn+1 = S + R Qn(a)

1

(c) Qn+1 = S + R'Qn(d) Qn+1 = S'+R'QnWhat is stored in the H & L general-purpose register? (CO3,K1) 1 1-e. Opcode (a) (b) Address of memory Address of next instruction (c) (d) Temporary data 1-f. After XRA A instruction is executed, what will be the status of Zero Flag? 1 (CO3,K1) 1 (a) (b) 0 (c) No change (d) none of these 1-g. Which register holds the next instruction to be executed in ATmega 328P? 1 (CO4,K1) SP (a) PC (b) SREG (c) (d) X register Which of the following addressing modes is NOT used by the AVR core in 1-h. 1 ATmega 328P? (CO4,K1) Immediate (a) Direct (b) Indirect (c) Absolute (d) How many timers does the ATmega328P microcontroller have? (CO5,K1) 1-i. 1 (a) 2 3 (b) (c) 4 (d) 5 In ATmega328P, what does an 8-bit timer mean? (CO5,K1) 1-j. 1 (a) The timer counts up to 8 The timer can count from 0 to 255 (b) The timer has 8 modes (c) The timer uses 8 pins (d) 2. Attempt all parts:-

(b)

2.a.

Qn+1 = S' + R Qn

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Define Prime, Essential Prime and Selective Prime implicants. (CO1,K2)

2 2 2 20				
2 2 20				
2				
20				
50				
6				
6				
6				
6				
) 6				
ow 6				
6				
50				
f 10				
ven 10 sing				
s 10				
(i) 10				
th 10				
10				
7. Answer any <u>one</u> of the following:-				
n 10				
10				

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- 8. Answer any one of the following:-
- 8-a. Describe the structure and purpose of the UCSRA (USART Control and Status 10 Register A) in ATmega328P. Discuss the functions of important bits like RXC (Receive Complete), TXC (Transmit Complete), and UDRE (USART Data Register Empty). (CO5,K3)

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8-b. Discuss about the ASSR and GTCCR register in detail. (CO5,K3)

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