

Ref: NIET/exam/2025/136

Date: 16.09.2025

Activity & Exam Schedule of Minor Degree Program

Semester-III (Session: 2025-26)

- Commencement of the program -27-09-2025

S. No.	Assignment (Online) (Start Date)	Assignment (Online) (Last date to Submit)	Quiz (Date of Conduction)
1	04-10-2025	11-10-2025	11-10-2025
2	18-10-2025	25-10-2025	25-10-2025
3	01-11-2025	08-11-2025	08-11-2025
4	15-11-2025	22-11-2025	22-11-2025
5	15-12-2025	22-12-2025	22-12-2025

Tentative Examination Schedule Semester- III (Session 2025-26)

Subject Code	Subject Name	Exam	Date	Time
AMSVL0351	Digital Integrated Circuit Lab	Internal Practical Examination	02-01-2026	10:00AM to 01:00PM
AMSEM0351	Modern Automotive Technology Lab			
AMSDS0351N	Introduction To Data Science Lab			
AMSML0351	Introduction to AI & Machine Learning Lab			
AMSVL0301	Digital Integrated Circuit	End Semester Theory Examination	03-01-2026	10:00AM to 01:00PM
AMSEM0301	Modern Automotive Technology			
AMSDS0301N	Introduction To Data Science			
AMSML0301	Introduction to AI & Machine Learning			
AMSVL0351	Digital Integrated Circuit Lab	External Practical Examination	06-01-2026	10:00AM to 01:00PM
AMSEM0351	Modern Automotive Technology Lab			
AMSDS0351N	Introduction To Data Science Lab			
AMSML0351	Introduction to AI & Machine Learning Lab			

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Registrar

Concerned Faculty Members

Students Notice Board

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Activity & Exam Schedule of Minor Degree Program

Semester-V (Session: 2025-26)

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- Commencement of the program -27-09-2025

S. No.	Assignment (Online) (Start Date)	Assignment (Online) (Last date to Submit)	Quiz (Date of Conduction)
1	04-10-2025	11-10-2025	11-10-2025
2	18-10-2025	25-10-2025	25-10-2025
3	01-11-2025	08-11-2025	08-11-2025
4	15-11-2025	22-11-2025	22-11-2025
5	15-12-2025	22-12-2025	22-12-2025

Tentative Examination Schedule Semester- V (Session 2025-26)

Subject Code	Subject Name	Exam	Date	Time
AMSVL0551	Digital Logic Design Using VHDL and Verilog Lab	Internal Practical Examination	02-01-2026	10:00AM to 01:00PM
AMSEM0551	Power drives and systems Lab			
AMSDS0551N	Analyzing, Visualizing, And Applying Data Science with Python Lab			
AMSML0551	Deep Learning and Neural Network Lab			
AMSVL0501	Digital Logic Design Using VHDL and Verilog	End Semester Theory Examination	03-01-2026	10:00AM to 01:00PM
AMSEM0501	Power drives and systems			
AMSDS0501N	Analyzing, Visualizing, And Applying Data Science with Python			
AMSML0501	Deep Learning and Neural Network			
AMSVL0551	Digital Logic Design Using VHDL and Verilog Lab	External Practical Examination	06-01-2026	10:00AM to 01:00PM
AMSEM0551	Power drives and systems Lab			
AMSDS0551N	Analyzing, Visualizing, And Applying Data Science with Python Lab			
AMSML0551	Deep Learning and Neural Network Lab			

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Activity & Exam Schedule of Minor Degree Program
Semester-VII (Session: 2025-26)

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- Commencement of the program -27-09-2025

S. No.	Assignment (Online) (Start Date)	Assignment (Online) (Last date to Submit)	Quiz (Date of Conduction)
1	04-10-2025	11-10-2025	11-10-2025
2	18-10-2025	25-10-2025	25-10-2025
3	01-11-2025	08-11-2025	08-11-2025
4	15-11-2025	22-11-2025	22-11-2025
5	15-12-2025	22-12-2025	22-12-2025

Tentative Examination Schedule Semester- VII (Session 2025-26)

Subject Code	Subject Name	Exam	Date	Time
AMSVL0751	Capstone Project	Internal Practical Examination	02-01-2026	10:00AM to 01:00PM
AMSEM0751	Capstone Project			
AMSDS0751	Capstone Project			
AMSM1.0751	Capstone Project			
AMSVL0701	VLSI Testing and Testability	End Semester Theory Examination	03-01-2026	10:00AM to 01:00PM
AMSEM0701	Automotive Power Grids			
AMSDS0701	Business Intelligence and Data Visualization			
AMSM1.0701	Applications of AI			
AMSVL0751	Capstone Project	External Practical Examination	06-01-2026	10:00AM to 01:00PM
AMSEM0751	Capstone Project			
AMSDS0751	Capstone Project			
AMSM1.0751	Capstone Project			

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